

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	2	"20020133702".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/16 10:47
S2	127126	(boot\$3 adj software) same4 microcontroller same4 (flash adj memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/29 07:44
S4	8969	S2 and ((software adj (copy adj protect\$3)) or authenticat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/29 07:44
S5	3044	S4 and (virtual or (boot adj sector))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/29 07:45
S6	462	S5 and (connect\$3 near port) and computer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/29 07:45
S7	133517	(boot\$3 adj software) same4 microcontroller same4 (flash adj memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/29 07:44
S8	9325	S7 and ((software adj (copy adj protect\$3)) or authenticat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/29 07:45
S9	3187	S8 and (virtual or (boot adj sector))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/29 07:45
S10	504	S9 and (connect\$3 near port) and computer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/29 07:46

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S11	1147	(713/193).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/29 07:46
S13	1271	(713/193).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/02/03 10:23
S15	327	S13 and ((boot\$3 same software) same4 (flash adj memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/02/03 10:26
S16	88	S15 and (copy same protect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/02/03 11:06
S18	28	S16 and USB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/02/03 13:05
S24	3436	(713/187,188,189,190,193).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/15 13:26
S25	4814	(711/103,115,152,163,164).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/15 13:28
S26	3270	(S24 or S25) and ((flash with memory) same4 boot\$3 and (copy adj protect\$3) and (microcontroller or microprocessor or CPU or processor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/15 13:33
S27	39	S26 and (software and (authoriz\$5 or authenticat\$3 or verif\$7) and (connect\$3 near port))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/15 14:26
S28	252	S26 and (embed\$4 with (processor or microprocessor or microcontroller or CPU))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/15 14:28

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S29	3436	(713/187,188,189,190,193).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/16 09:25
S30	4814	(711/103,115,152,163,164).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/16 09:25
S31	3270	(S29 or S30) and ((flash with memory) same4 boot\$3 and (copy adj protect\$3) and (microcontroller or microprocessor or CPU or processor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/16 09:25
S32	252	S31 and (embed\$4 with (processor or microprocessor or microcontroller or CPU))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/16 09:26
S33	98	S32 and boot\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2007/03/16 09:26